

Fig. 1

1. CONTROLLER

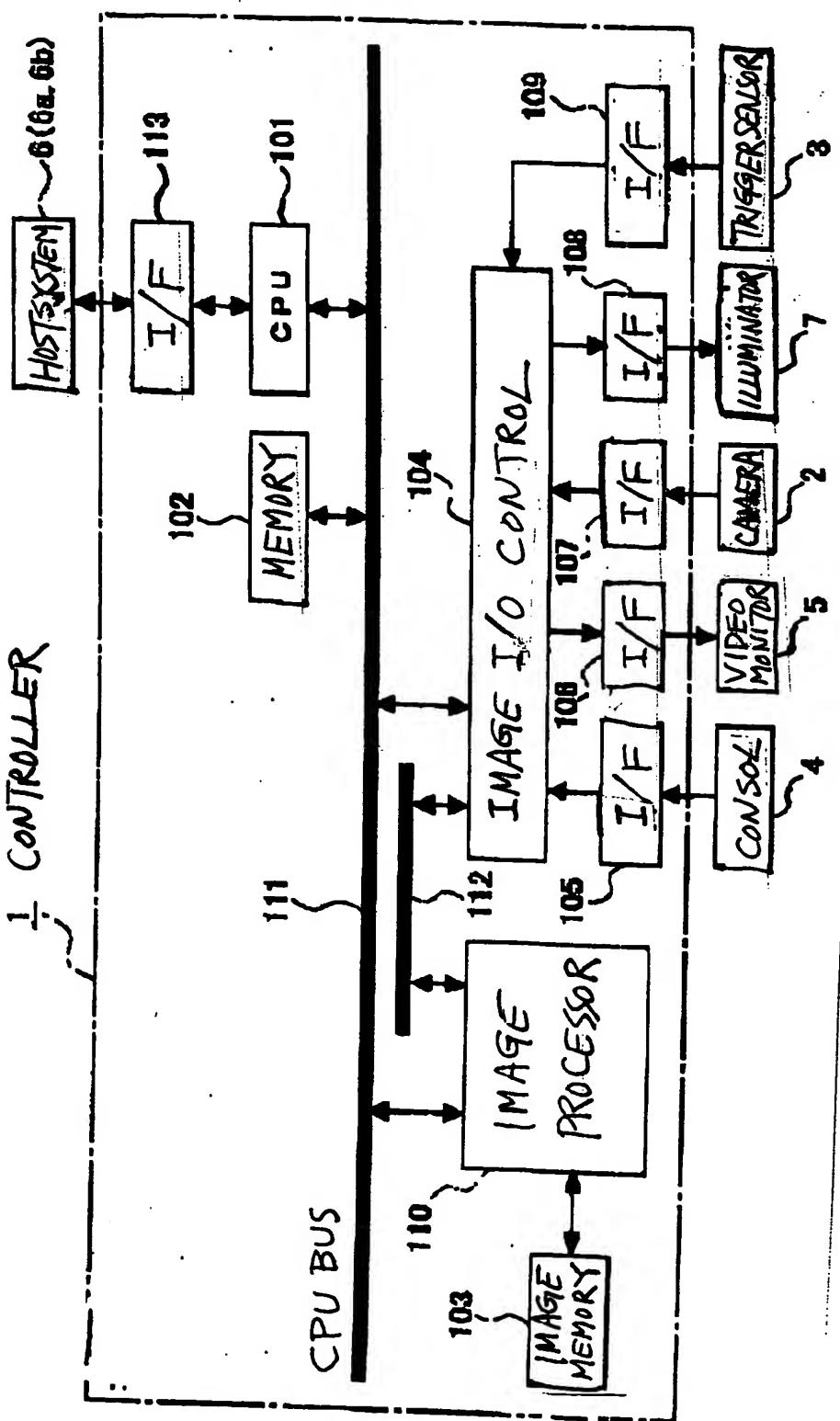


Fig. 2

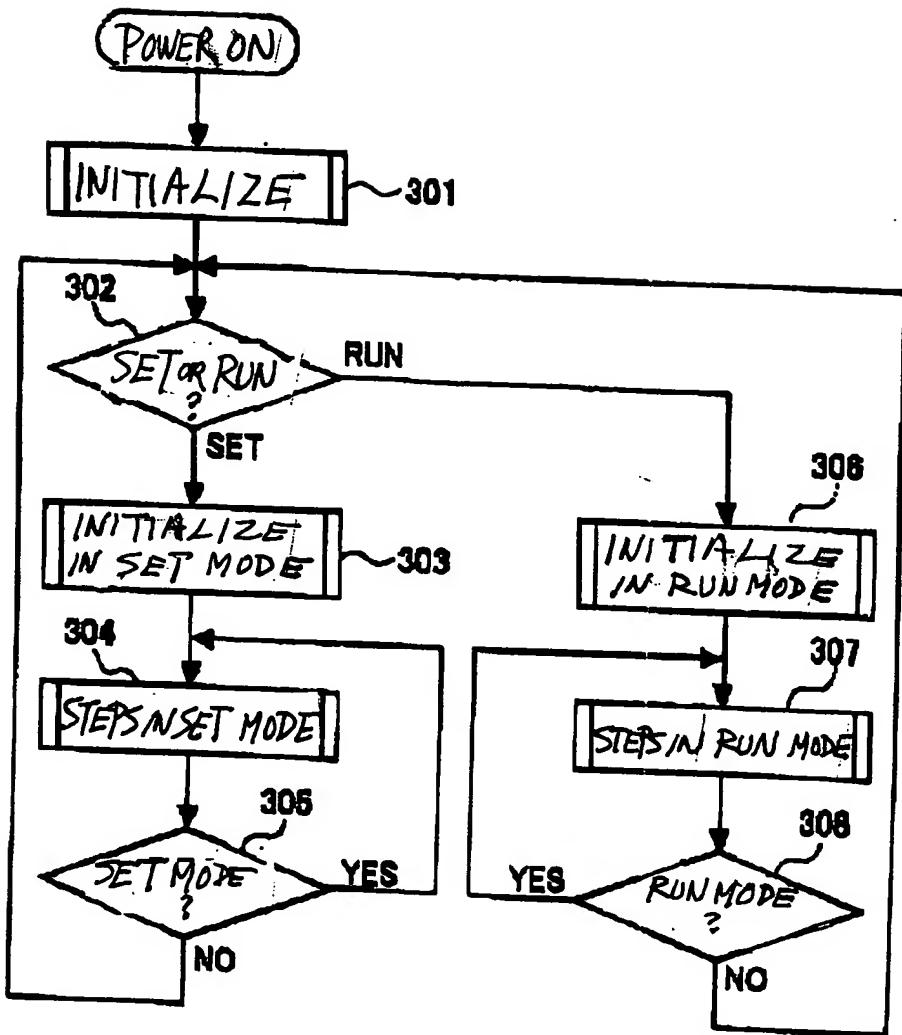


Fig. 3

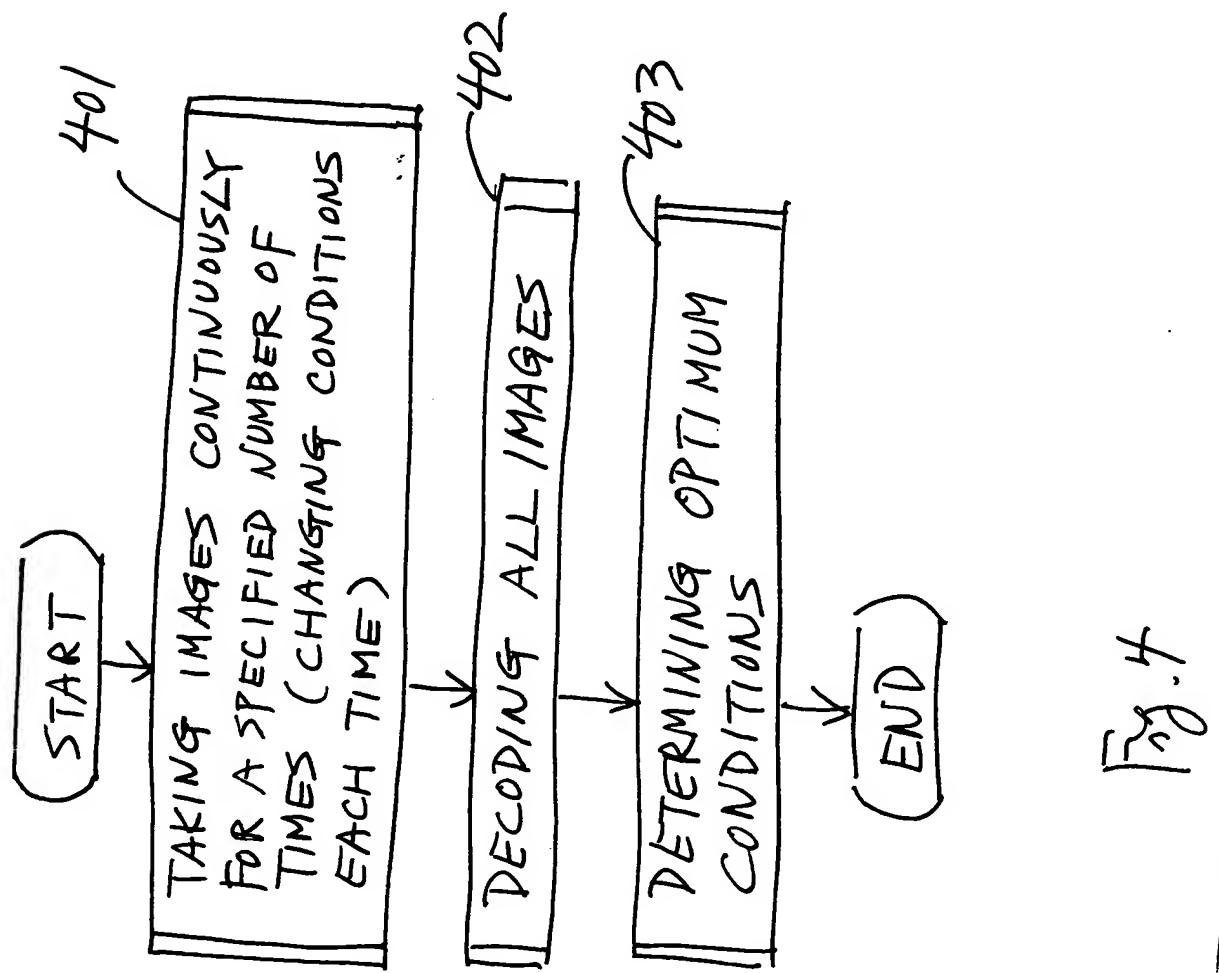
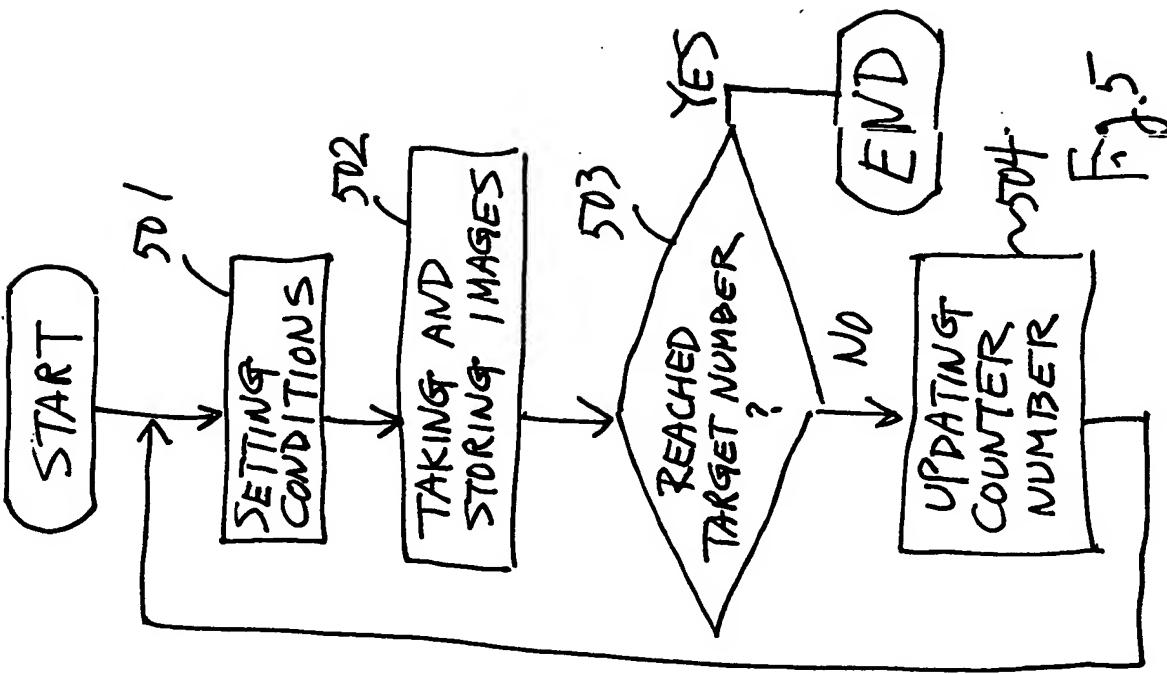


Fig. 4

Fig. 5

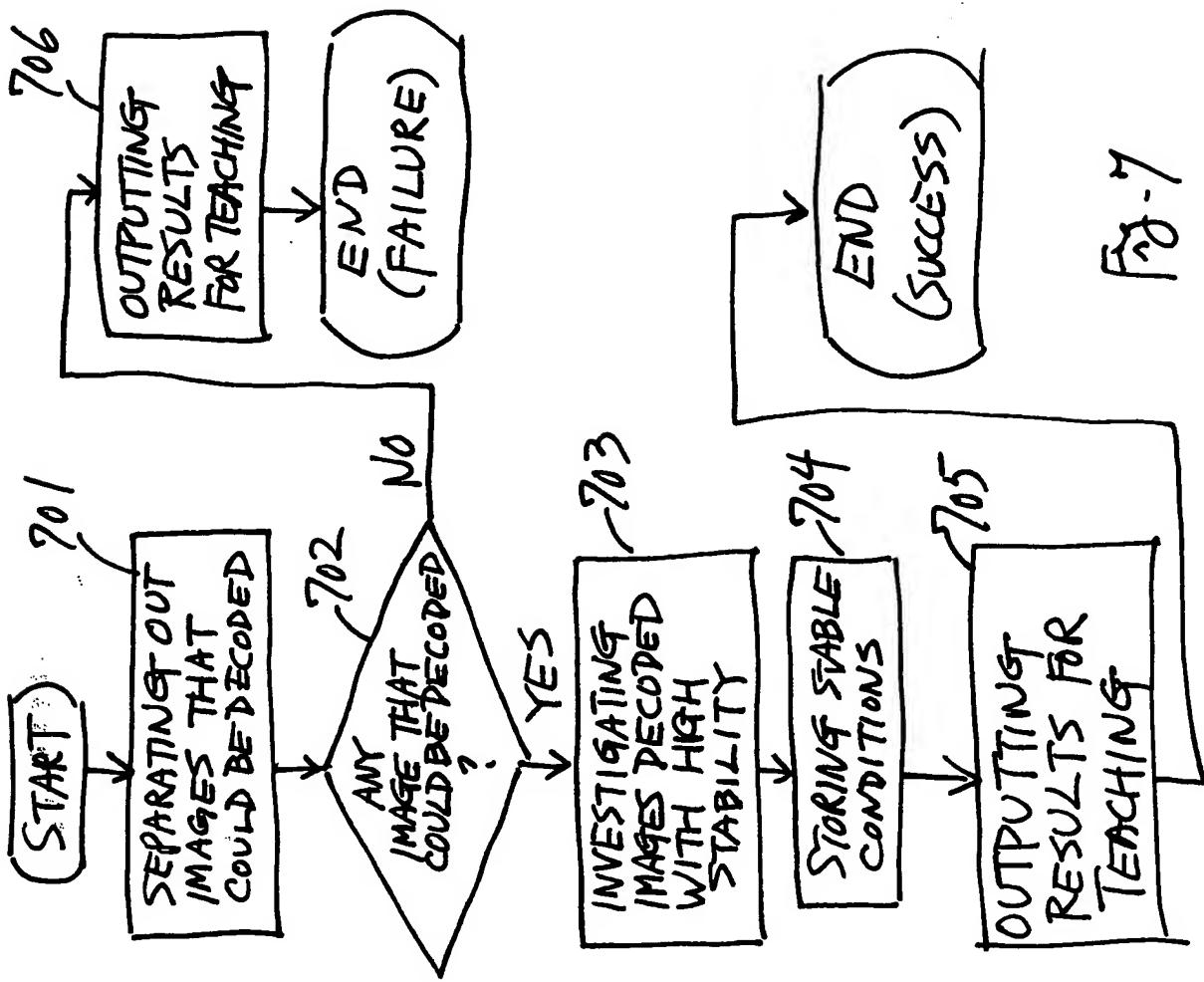


Fig. 7

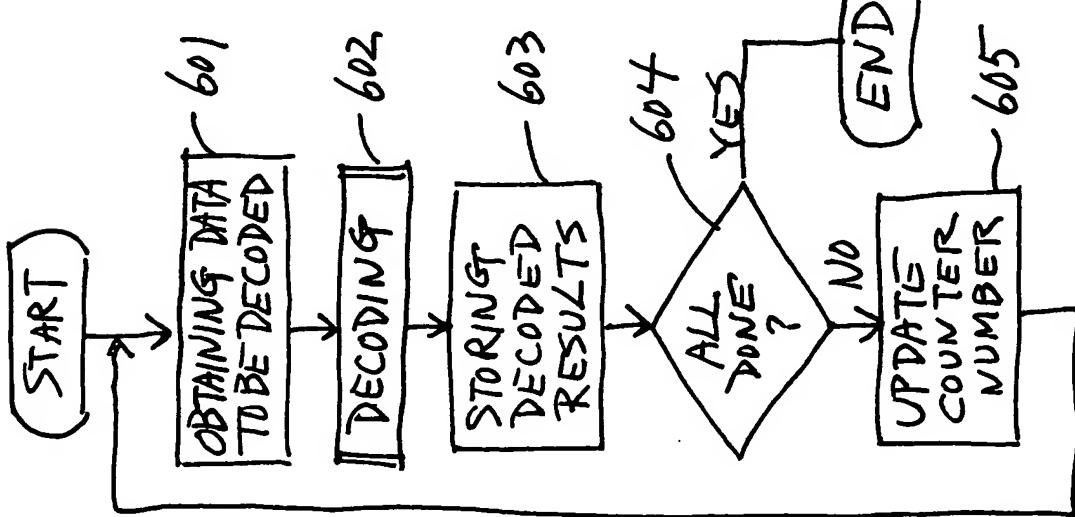


Fig. 6

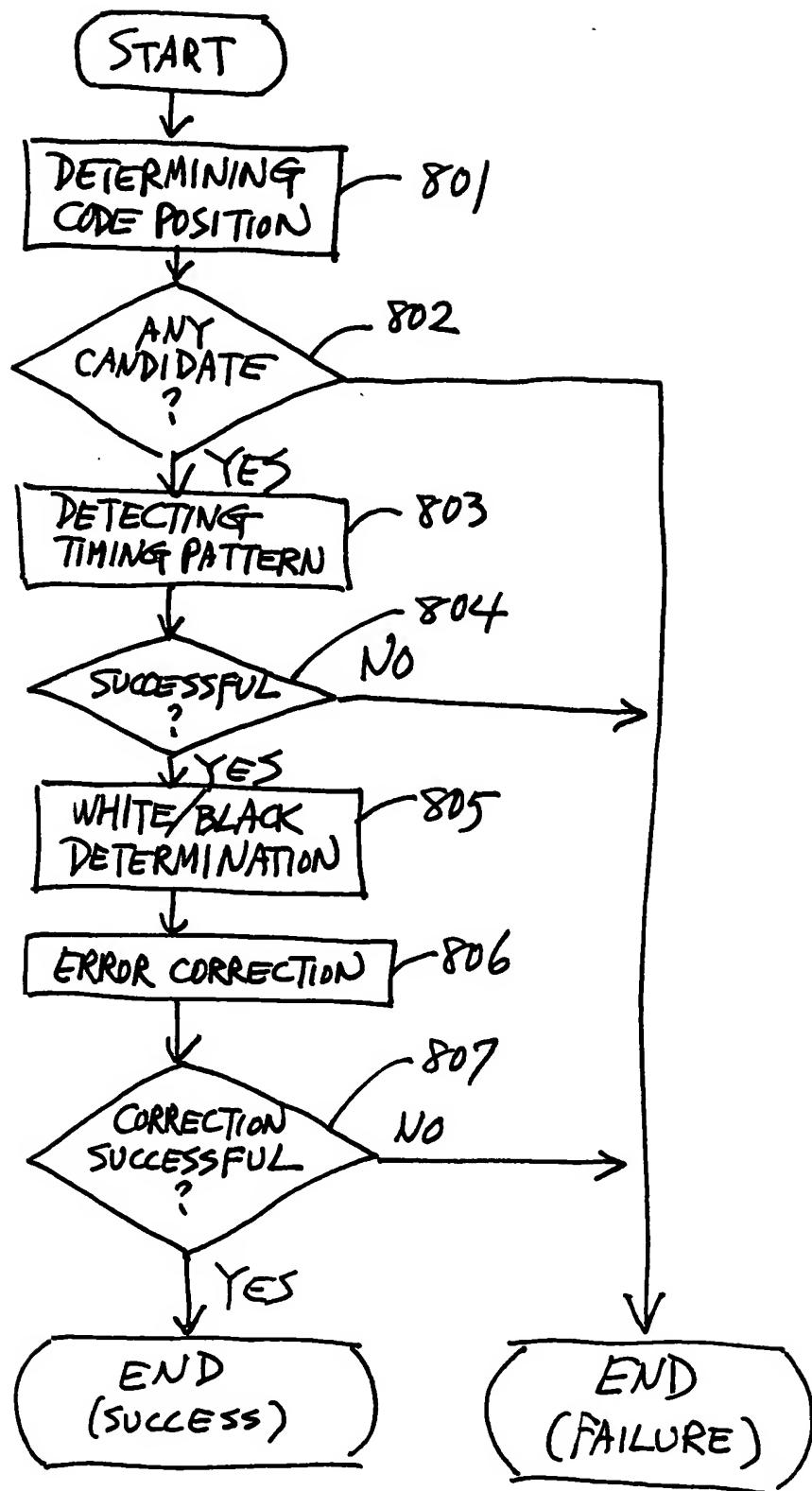


Fig. 8

TEACHING: SUCCESSFUL

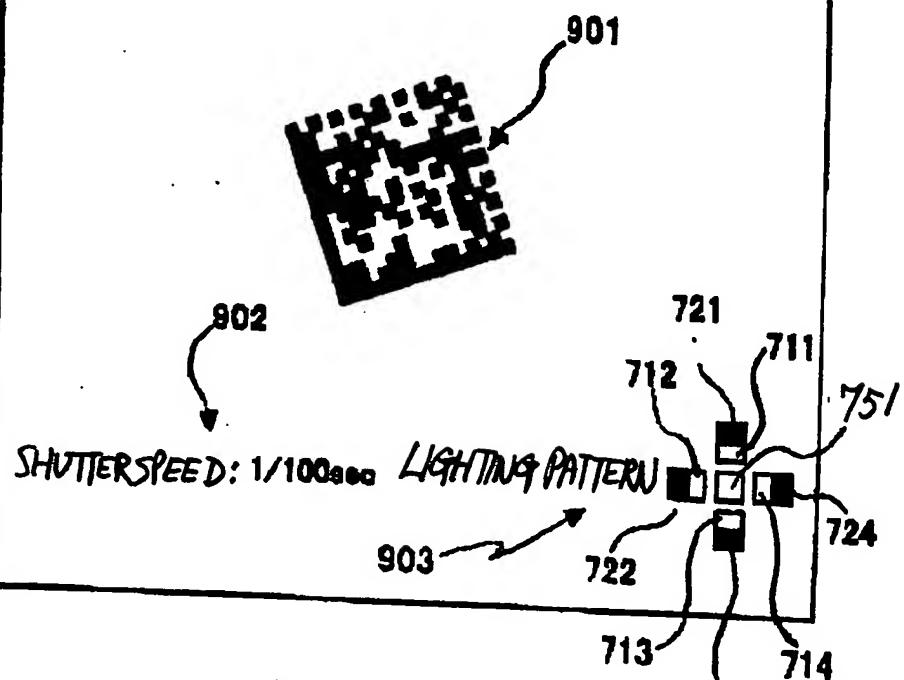


Fig. 9

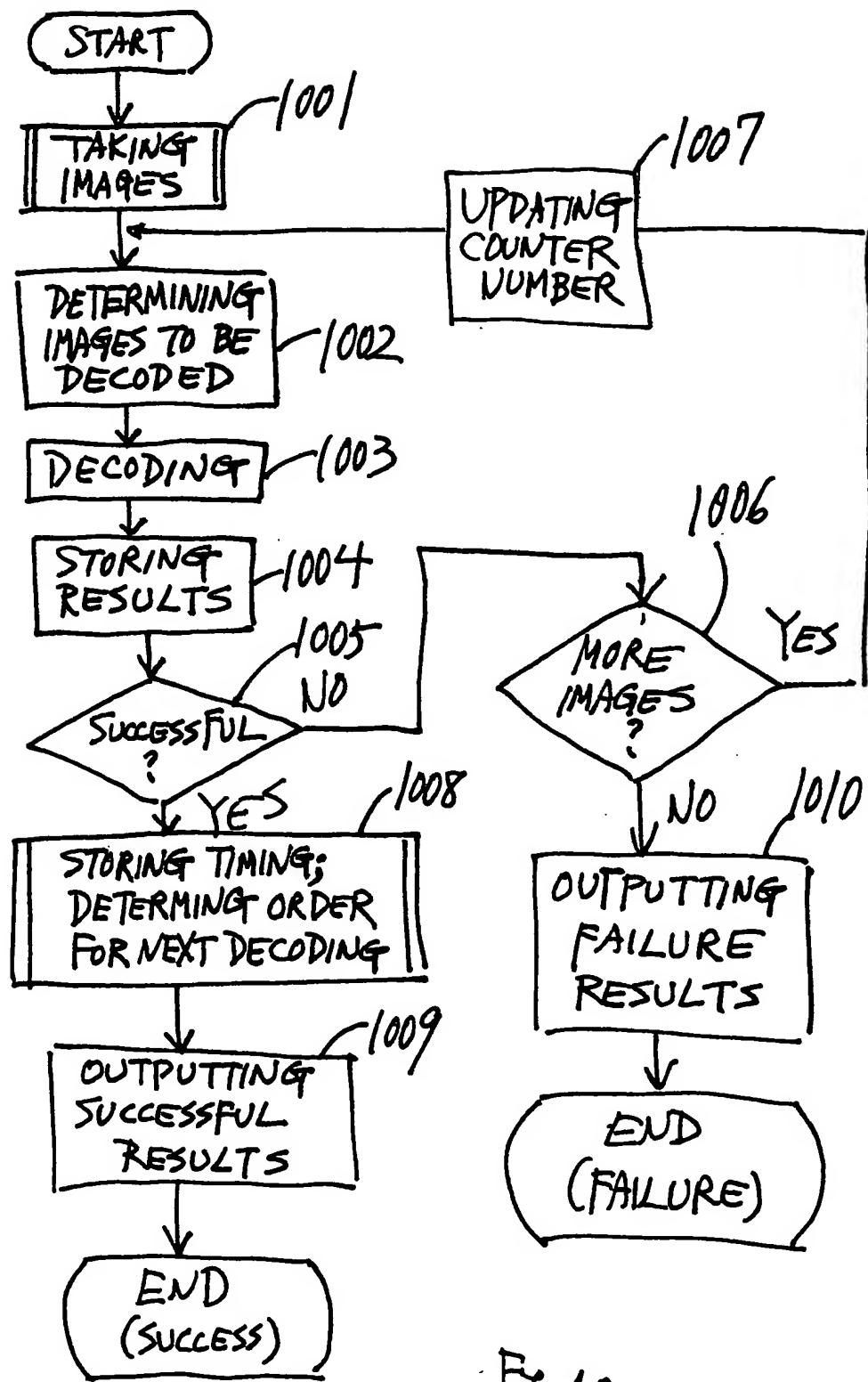


Fig.10

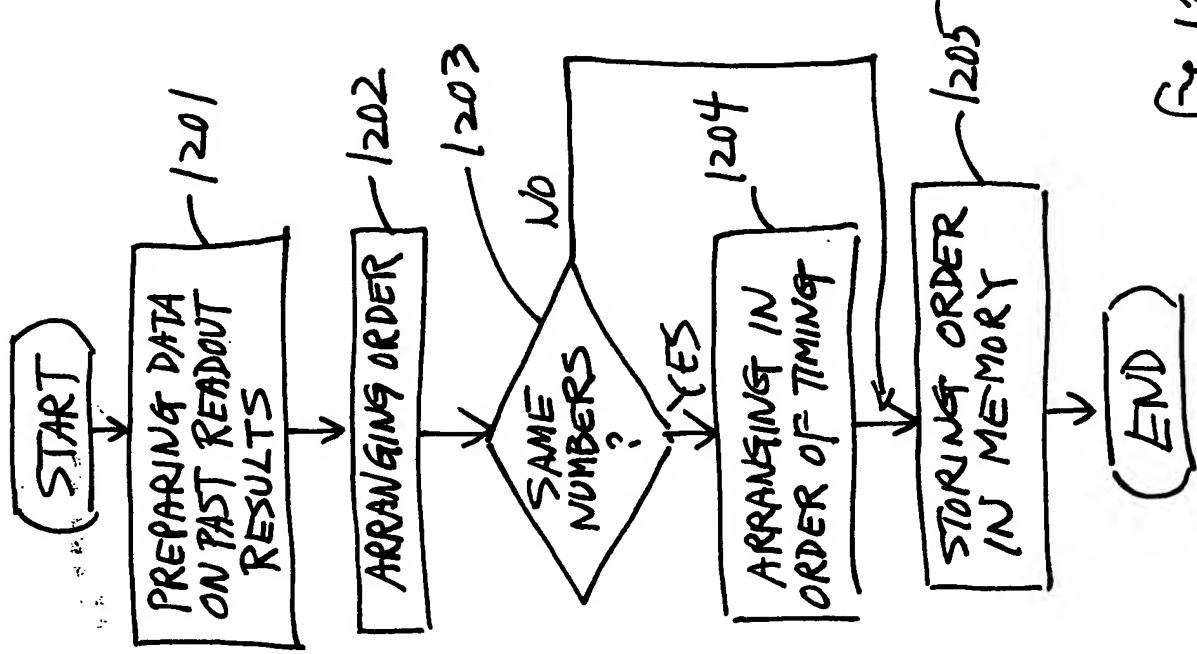


Fig. 12

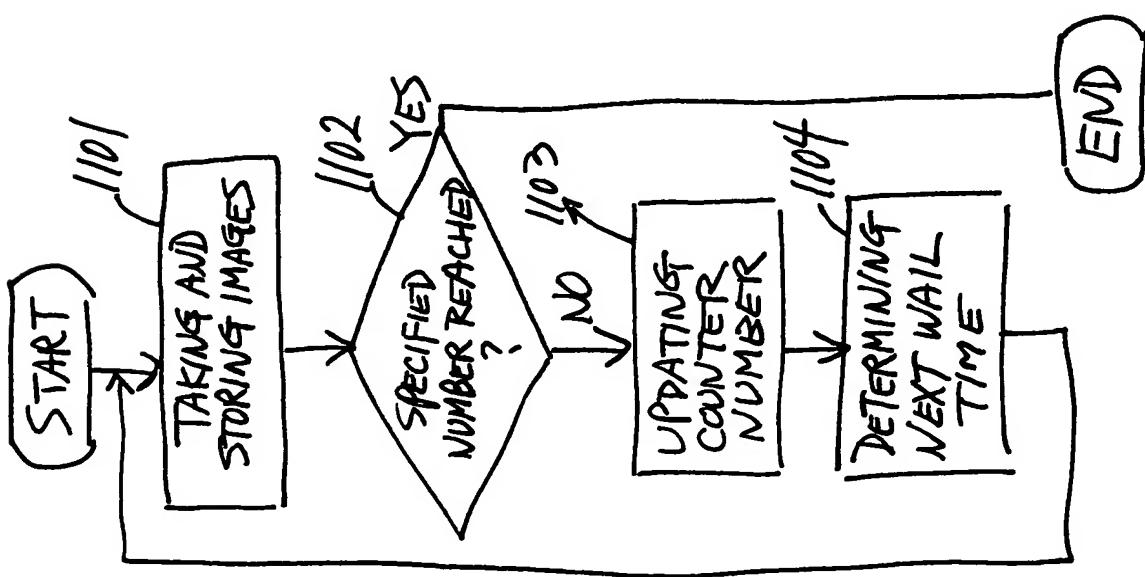


Fig. 11

IMAGE No.	RESULT OF PAST READ OUT							TOTAL SUCCESS
	100 TH	99 TH	98 TH	97 TH	96 TH	95 TH	94 TH	
1								0
2								0
3	FAILURE	SUCCESS						1
4	FAILURE	FAILURE	SUCCESS					9
5	SUCCESS	SUCCESS		SUCCESS				85
6				SUCCESS				5
7								0

Fig. 13

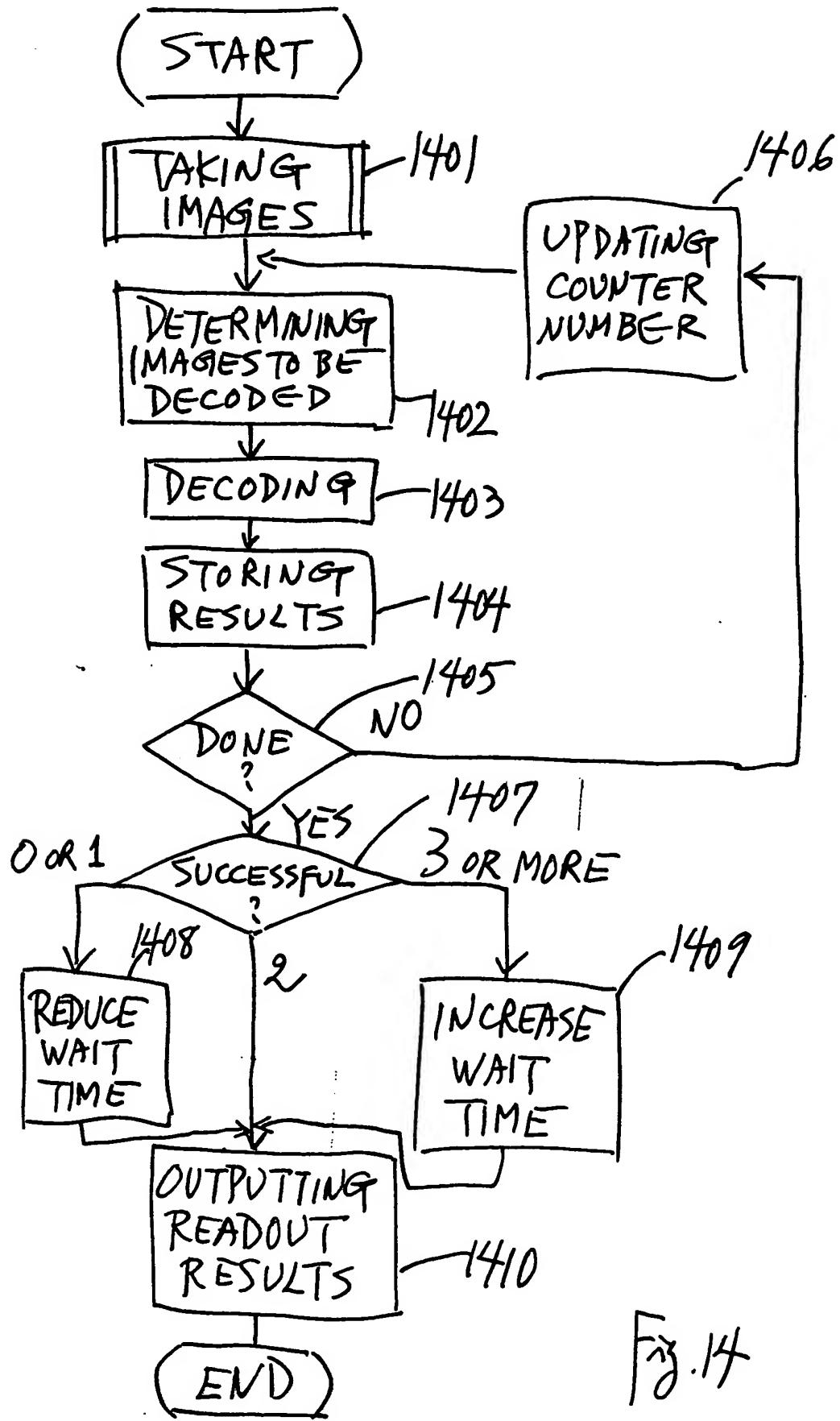


Fig.14

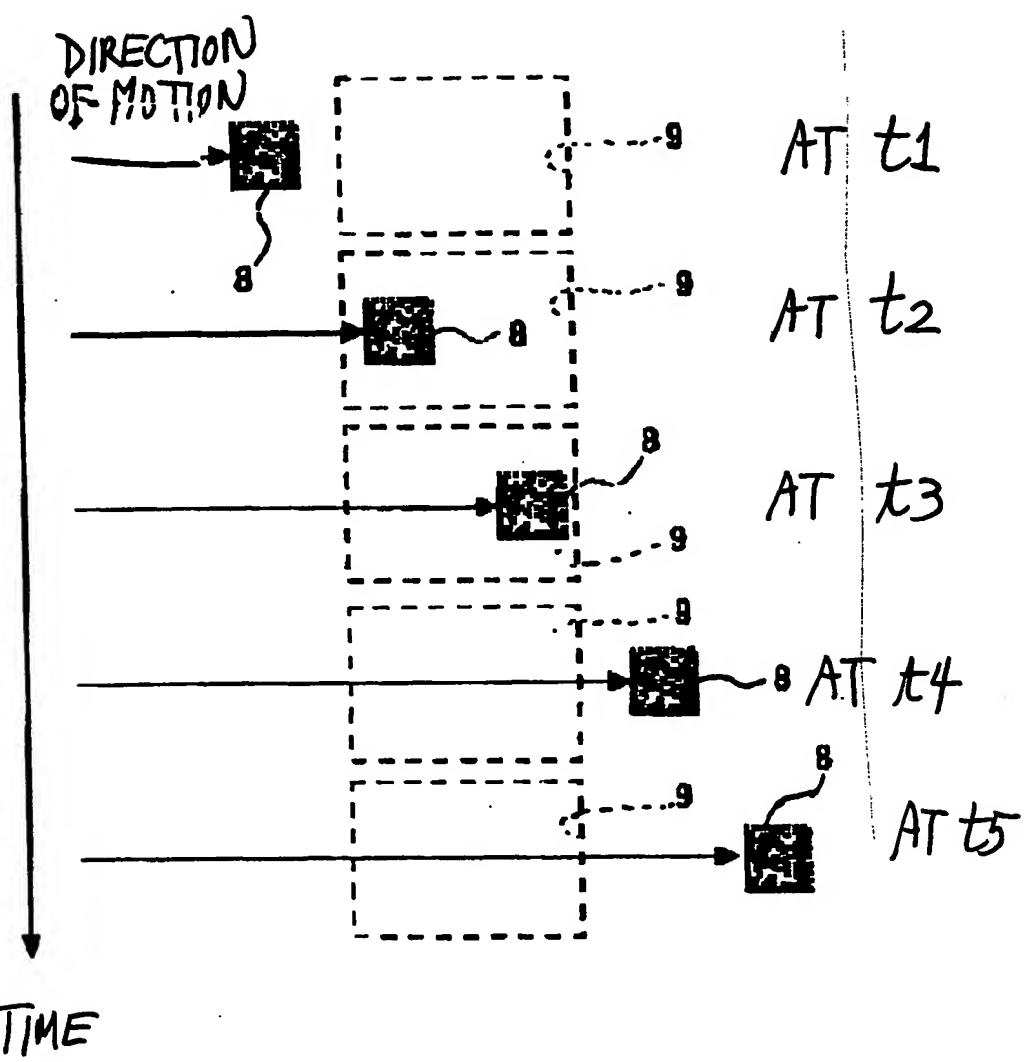


Fig. 15

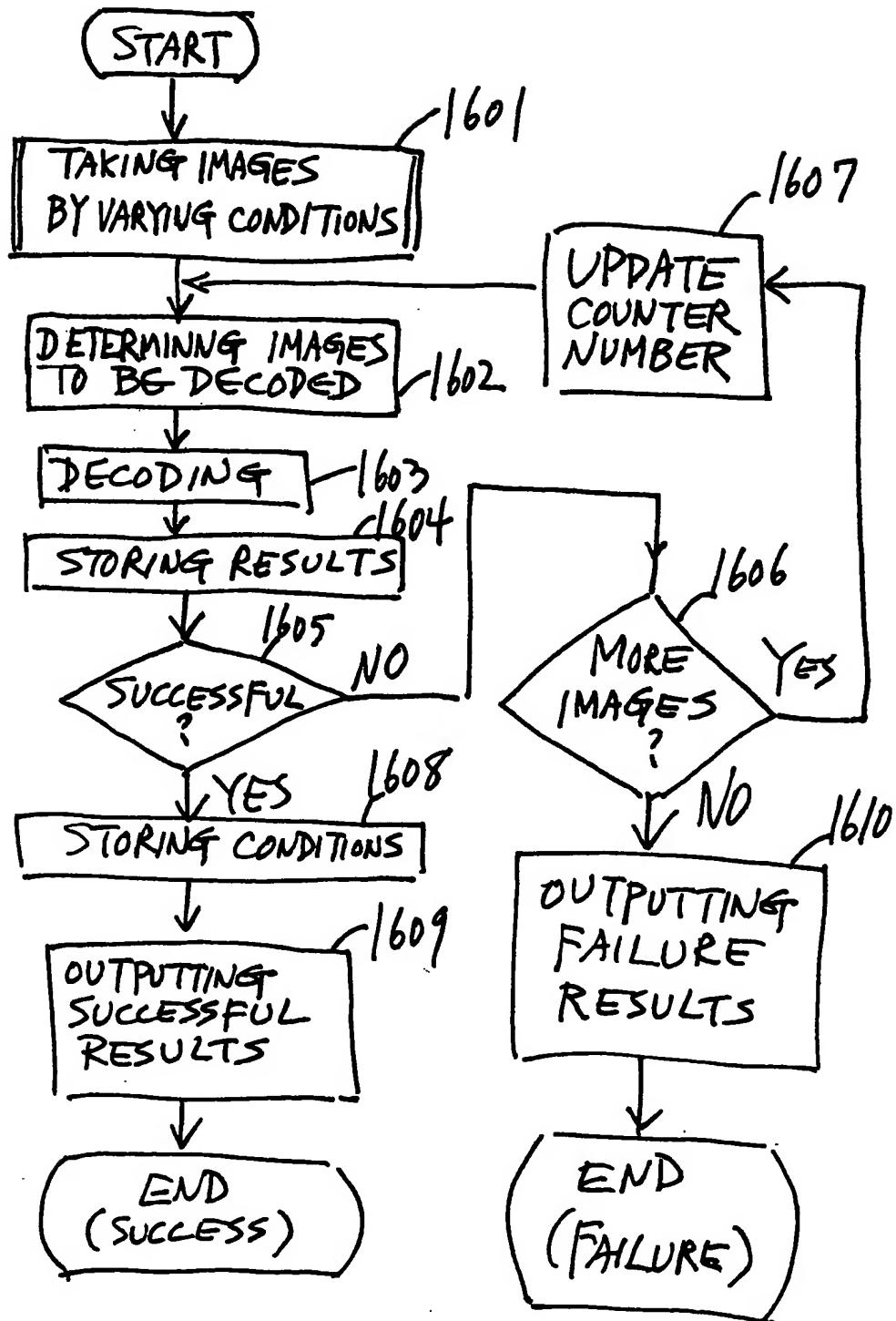


Fig.16

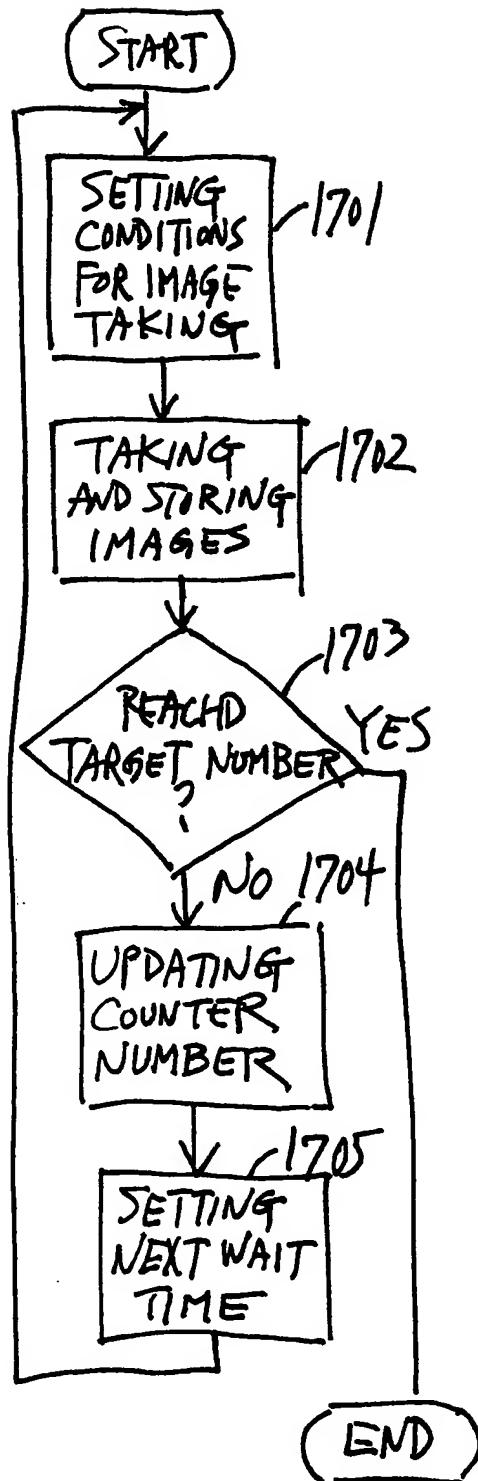


Fig. 17

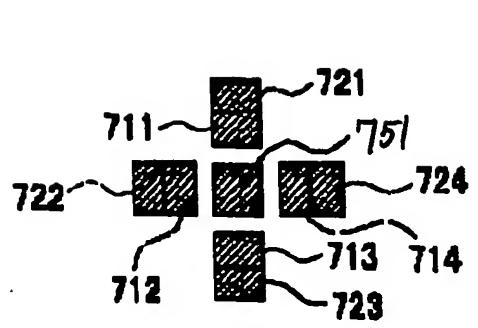


Fig. 18A

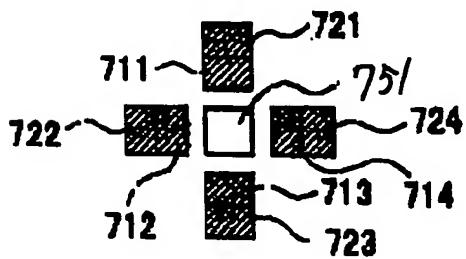


Fig. 18B

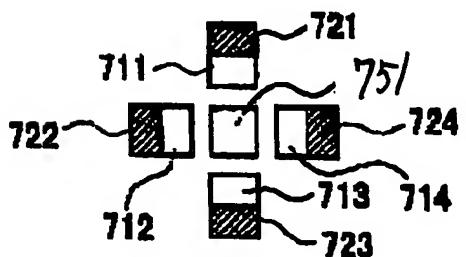


Fig. 18C

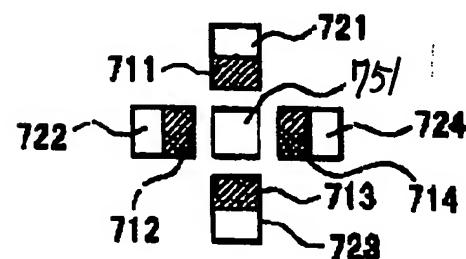


Fig. 18D

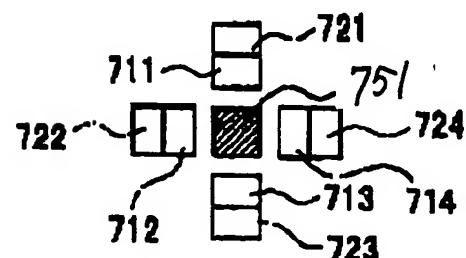


Fig. 18E

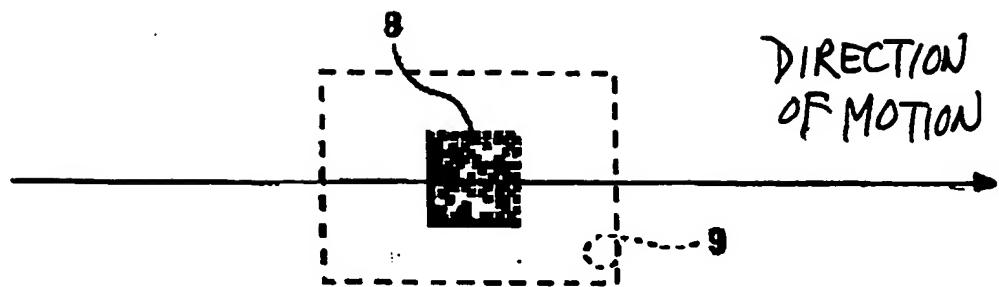


Fig. 19A
(PRIOR ART)

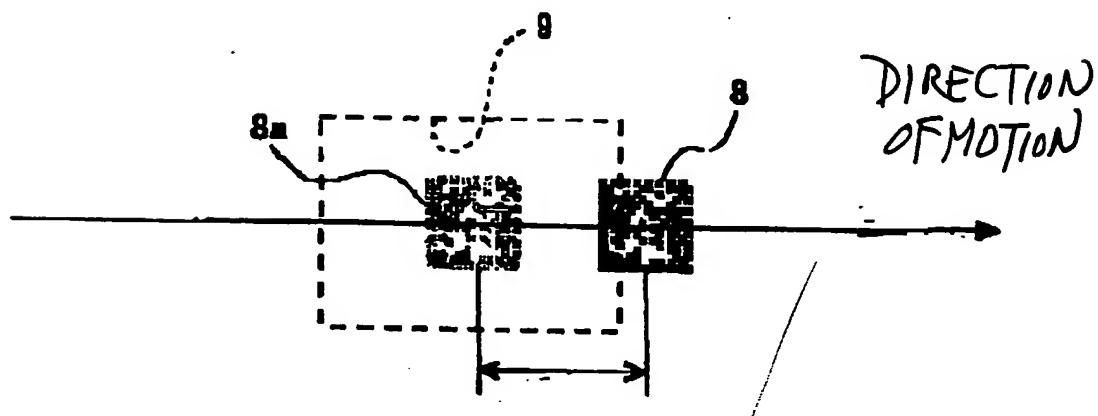


Fig. 19B
(PRIOR ART)